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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/037,680	01/04/2002	Theodore F. Emerson	COMP:0228 P00-3578	8297	
Intellectual Property Administration Legal Department, M/S 35 P.O. Box 272400 Ft Collins, CO 80527-2400			EXAMINER		
			NGUYEN, THU HA T		
			ART UNIT	PAPER NUMBER	
			2155		
			DATE MAILED: 12/15/2005	DATE MAILED: 12/15/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
Office Action Commons	10/037,680	EMERSON ET AL.					
Office Action Summary	Examiner	Art Unit					
	Thu Ha T. Nguyen	2155					
The MAILING DATE of this communica Period for Reply	tion appears on the cover sheet with	the correspondence address					
A SHORTENED STATUTORY PERIOD FOR WHICHEVER IS LONGER, FROM THE MAII - Extensions of time may be available under the provisions of 3 after SIX (6) MONTHS from the mailing date of this communication of the period for reply is specified above, the maximum statuted Failure to reply within the set or extended period for reply will, Any reply received by the Office later than three months after earned patent term adjustment. See 37 CFR 1.704(b).	LING DATE OF THIS COMMUNICATION OF THIS COMMUNICATION OF THE STATE OF	ATION. ly be timely filed 15 from the mailing date of this communication. NDONED (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed of	Responsive to communication(s) filed on <u>01/4/02-04/15/02</u> .						
	☑ This action is non-final.						
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,—	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
closed in accordance with the practice	under Ex parte Quayre, 1905 C.D.	11, 40,0 O.G. 210.					
Disposition of Claims							
)⊠ Claim(s) <u>1-20</u> is/are pending in the application.							
4a) Of the above claim(s) is/are	4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.							
6) Claim(s) <u>1-,3,6-10, 12-20</u> is/are rejected.							
8) Claim(s) are subject to restriction	n and/or election requirement						
3, <u> </u>	and/or disolitativequitations.						
Application Papers							
9)☐ The specification is objected to by the E	xaminer.						
10) The drawing(s) filed on is/are: a)	☐ accepted or b)☐ objected to by	the Examiner.					
Applicant may not request that any objectio							
Replacement drawing sheet(s) including the							
11) The oath or declaration is objected to by							
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for a) All b) Some * c) None of: 1. Certified copies of the priority doce 2. Certified copies of the priority doce 3. Copies of the certified copies of the application from the International * See the attached detailed Office action for the certified copies of the certified copies of the attached detailed Office action for the certified copies of th	cuments have been received. cuments have been received in App he priority documents have been re Bureau (PCT Rule 17.2(a)).	olication No eceived in this National Stage					
Attachment(s)							
Notice of References Cited (PTO-892)	4) 🔲 Interview Sun	nmary (PTO-413)					
2) 🔲 Notice of Draftsperson's Patent Drawing Review (PTO-	948) Paper No(s)/N	Mail Date					
B) Information Disclosure Statement(s) (PTO-1449 or PTO Paper No(s)/Mail Date <u>04/15/02</u> .	0/SB/08) 5) ☐ Notice of Info 6) ☐ Other:	rmal Patent Application (PTO-152)					
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DETAILED ACTION

1. Claims **1-20** are presented for examination.

Claim Objections

2. Claims 1, 3, 10, 12-14 are objected to because of the following informalities:

Claims 1, and 10 recited the word "IOP" which is abbreviation. Applicant is requested to write "IOP" in the complete form. Likewise, applicant is required to make the same correction for other dependent claims. Appropriate correction is required.

Claim 3, which is depended on claim 1, recited the limitation "the plurality of registers". There is insufficient antecedent basis for this limitation in this claim.

Appropriate correction is required.

Claims 12-14, which is depended on claim 10, recited the limitation "the plurality of registers". There is lack of antecedent basis for this limitation in these claims.

Appropriate correction is required.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

- 4. Claims 1, 3, 6-10, and 12-20 are rejected under 35 U.S.C. § 103 (a) as being unpatentable over **Crump et al.** (hereinafter Crump) U.S. Patent No. **5,752,044**, in view of **Fasack et al.** (hereinafter Fasack) U.S. Patent No. **4,823,290**.
- 5. As to claim 1, **Crump** teaches the invention as claimed, including a remote server management controller, comprising:

an IOP that is adapted to communicate with a managed server via a communication interface (col. 6, line 58-col. 8, line 15);

a failsafe register that is adapted to receive an IOP reset request via the communication interface (col. 39, line 36-col. 40, line 32, col. 41, line 49-col. 42, line 9);

a failsafe boot timer adapted to initiate a countdown when the failsafe register receives an IOP reset request (col. 26, line 28-62, col. 27, line 10-col. 28, line 60, col. 30, lines 7-28, col. 31, line 32-col. 32, line 15).

However, **Crump** does not explicitly teach wherein the IOP is adapted to be placed in a reset condition if the IOP does not clear the IOP reset request from the failsafe register and disable the failsafe boot-timer before the countdown expires.

Fasack teaches wherein the IOP is adapted to be placed in a reset condition if the IOP does not clear the IOP reset request from the failsafe register and disable the failsafe boot timer before the countdown expires (col. 17, lines 7-47).

It would have been obvious to one of ordinary skill in the Data Processing art at the time of the invention was made to incorporate the feature of the IOP is adapted to be placed in a reset condition if the IOP does not clear the IOP reset request from the Application/Control Number: 10/037,680

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failsafe register and disable the failsafe boot timer before the countdown expires, as disclosed by **Fasack**, into **Crump** system because it provide an efficient system for monitoring the environment in the computer system operations to provide early warning of environment conditions (see Fasack col. 1, lines 7-13).

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- 6. As to claim 3, **Crump** teaches the remote server management controller of claim 1 wherein the IOP is adapted to execute an initialization sequence and to place the plurality of registers in the locked condition as part of the initialization sequence (col. 22, lines 4-21, col. 30, lines 6-28, col. 31, line 66-col. 32, line 11).
- 7. As to claim 6, **Crump** teaches the remote server management controller of claim 1 wherein the IOP is adapted to decline the IOP reset request (col. 48, line 57-col. 49, line 25).
- 8. As to claim 7, **Crump** teaches the remote server management controller of claim 1 wherein the communication interface is a PCI bus (col. 7, line 50-col. 8, line 5).
- 9. As to claim 8, **Crump** teaches the remote server management controller of claim 1 wherein cycles received via the communication interface are retried until the IOP completes an initialization sequence (col. 22, lines 4-21, col. 30, lines 6-28, col. 31, line 66-col. 32, line 11).

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10. As to claim 9, **Crump** teaches the remote server management controller of claim 1 wherein cycles received via the communication interface are retried until the IOP is placed in a reset state upon expiration of the failsafe boot timer (col. 26, line 28-62, col. 27, line 10-col. 28, line 60, col. 30, lines 7-28, col. 31, line 32-col. 32, line 15).

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- 11. As to claim 10, **Crump** teaches the invention as claimed, including a managed server, comprising:
 - a communication interface (col. 7, line 50-col. 8, line 5);
- a device adapted to communicate via the communication interface (figure 3, col. 6, line 34-col. 8, line 15):
 - a remote server management controller, comprising:
- an IOP that is adapted to communicate via the communication interface (col. 6, line 58-col. 8, line 15);
- a failsafe register that is adapted to receive an IOP reset request via the communication interface (col. 39, line 36-col. 40, line 32, col. 41, line 49-col. 42, line 9);
- a failsafe boot timer adapted to initiate a countdown when the failsafe register receives an IOP reset request (col. 26, line 28-62, col. 27, line 10-col. 28, line 60, col. 30, lines 7-28, col. 31, line 32-col. 32, line 15); and

However, **Crump** does not explicitly teach wherein the IOP is adapted to be placed in a reset condition if the IOP does not clear the IOP reset request from the failsafe register and disable the failsafe boot timer before the countdown expires.

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Fasack teaches wherein the IOP is adapted to be placed in a reset condition if the IOP does not clear the IOP reset request from the failsafe register and disable the failsafe boot timer before the countdown expires (col. 17, lines 7-47).

It would have been obvious to one of ordinary skill in the Data Processing art at the time of the invention was made to incorporate the feature of the IOP is adapted to be placed in a reset condition if the IOP does not clear the IOP reset request from the failsafe register and disable the failsafe boot timer before the countdown expires, as disclosed by **Fasack**, into **Crump** system because it provide an efficient system for monitoring the environment in the computer system operations to provide early warning of environment conditions (see Fasack col. 1, lines 7-13).

- 12. As to claim 12, **rump** teaches the managed server of claim 10 wherein the IOP is adapted to execute an initialization sequence and to place the plurality of registers in the locked condition as part of the initialization sequence (col. 22, lines 4-21, col. 30, lines 6-28, col. 31, line 66-col. 32, line 11).
- 13. As to claim 13, **Crump** teaches the managed server of claim 10 wherein at least one of the plurality of registers is placed in the locked condition unless the managed server is operating in a system management mode (col. 22, lines 4-21, col. 30, lines 6-28, col. 31, line 66-col. 32, line 11).

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14. As to claim 14, **Crump** teaches the managed server of claim 10 wherein the IOP is adapted to grant permission to access at least one of the plurality of registers (col. 26, line 28-62, col. 27, line 10-col. 28, line 60, col. 30, lines 7-28, col. 31, line 32-col. 32, line 15).

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- 15. As to claim 15, **Crump** teaches the managed server of claim 10 wherein the IOP is adapted to decline the IOP reset request (col. 48, line 57-col. 49, line 25).
- 16. As to claim 16, **Crump** teaches the managed server of claim 10 wherein the communication interface is a PCI bus (col. 7, line 50-col. 8, line 5).
- 17. As to claim 17, **Crump** teaches the managed server of claim 10 wherein cycles received via the communication interface are retried until the IOP completes an initialization sequence (col. 22, lines 4-21, col. 30, lines 6-28, col. 31, line 66-col. 32, line 11).
- 18. As to claim 18, **Crump** teaches the invention as claimed, including a method of authorizing access to a register, comprising the acts of:

receiving a reset request via a failsafe register (col. 39, line 36-col. 40, line 32, col. 41, line 49-col. 42, line 9);

initiating a countdown of predetermined duration in response to the reset request (col. 26, line 28-62, col. 27, line 10-col. 28, line 60, col. 30, lines 7-28, col. 31, line 32-col. 32, line 15);

permitting access to the register if the reset request is not cleared and the countdown is not halted before expiration of the predetermined duration ().

However, **Crump** does not explicitly teach the feature of permitting access to the register if the reset request is not cleared and the countdown is not halted before expiration of the predetermined duration.

Fasack teaches permitting access to the register if the reset request is not cleared and the countdown is not halted before expiration of the predetermined duration (col. 17, lines 7-47).

It would have been obvious to one of ordinary skill in the Data Processing art at the time of the invention was made to incorporate the feature of permitting access to the register if the reset request is not cleared and the countdown is not halted before expiration of the predetermined duration, as disclosed by **Fasack**, into **Crump** system because it provide an efficient system for monitoring the environment in the computer system operations to provide early warning of environment conditions (see Fasack col. 1, lines 7-13).

19. As to claim 19, **Crump** teaches the method of claim 18 further comprising the act of denying access to the register (col. 48, line 57-col. 49, line 25).

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20. As to claim 20, **Crump** teaches the method of claim 18 wherein the act of denying access to the register is performed as part of an initialization process (col. 48, line 57-col. 49, line 25).

Allowable Subject Matter

Claims 2, 4-5 and 11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

21. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

Sutherland et al. (USPN 2003/0177216), discloses system and method for isolation technique for networks.

Crump et al. (USPN 5,560,023) discloses system and method for automatic backup system for advanced power management.

Pecore (USPN 5,860,028) discloses I/O bus expansion system wherein processor checks plurality of possible address until a response from peripheral selected by address decoder using user input.

Horst et al. (USPN 6,496,940) discloses multiple processor system with standby sparing.

Byers et al. (USPN 5,781,770) discloses system and method for controlling shutdown of a processing unit.

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22. Any inquiry concerning this communication or earlier communications from

the examiner should be directed to Thu Ha Nguyen, whose telephone number is (571)

272-3989. The examiner can normally be reached Monday through Friday from 8:30

AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Najjar Saleh, can be reached at (571) 272-4006.

The fax phone numbers for the organization where this application or proceeding

is assigned are (571) 273-8300 for regular communications.

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Business Center (EBC) at 866-217-9197 (toll-free).

ThuHa Nguyen

Patent Examiner

December 12, 2005

Thuhanguyr